

# A Startup Speed Sensor less Method of an Induction Motor by using a Modular Multilevel Cascaded Inverter based on double star chopper cell

Mr. Hadole A.V.<sup>1</sup>, Prof. Rane P.R.<sup>2</sup>

<sup>1,2</sup> P.R. Pote(Patil) College of Engineering, Maharashtra ,India

**Abstract-** This paper gives theoretical and experimental consideration on a practical speed-sensor less start-up method for an induction motor driven by a modular multilevel cascade inverter based on double-star chopper cells from standstill to middle speed. This motor drive is suitable, particularly for a large-capacity fan- or blower-like load. The load torque is proportional to a square of the motor mechanical speed. The start-up method is characterized by combining capacitor-voltage control with motor-speed control. The motor-speed control with the minimal stator current plays a crucial role in eliminating a speed sensor from the drive system and in reducing an a voltage fluctuation occurring across each dc capacitor. Experimental results obtained from the 400-V 15-kW downscaled system with no speed sensor verify that the motor-speed control proposed for the DSCC-based drive system can enhance the start-up torque by a factor of three under the same ac-voltage fluctuation. Several start-up waveforms show stable performance from standstill to middle speed with different load torques.

**Keywords-** Medium-voltage induction motor drives, minimal stator current, modular multilevel cascade inverters, speed sensor less start-up method.

## I. INTRODUCTION

Attention has been paid to medium-voltage motor drives for energy savings without regenerative brakes, A modular multilevel cascade inverter based on double star chopper cells (MMCI-DSCC) has been expected as one of the next-generation medium-voltage multilevel pulse width modulation (PWM) inverters for such motor drives. For the sake of simplicity, the MMCI-DSCC is referred to as the “DSCC” in this paper. Each leg of the DSCC consists of two positive and negative arms and a center-tapped inductor sitting between the two arms. Each arm consists of multiple bidirectional dc/dc choppers called as “chopper cells.” The low voltage sides of the chopper cells are connected in cascade, while the electrically floating high-voltage sides of chopper cells are equipped with a dc capacitor and a voltage sensor. A synergy effect of lower voltage steps and phase-

shifted PWM leads to lower harmonic voltage and current, as well as lower EMI emission, as the count of cascaded chopper cells per leg increases. The power conversion circuit of the DSCC is so flexible in design that any count of cascaded chopper cells is theoretically possible. When a DSCC is applied to an ac motor drive, the DSCC would suffer from ac-voltage fluctuations in the dc-capacitor voltages of each chopper cell in a low-speed range, because the ac-voltage fluctuation gets more serious as a stator-current frequency gets lower. Hence, the fluctuation should be attenuated satisfactorily to achieve stable low-speed and start-up performance. Several papers have exclusively discussed startup methods for DSCC-based induction motor drives.

The aim of this paper is to verify the effectiveness and practicability of a speed-sensor less start-up method for a DSCC based induction motor drive, in which the motor starts rotating from standstill to middle speed with a ramp change. This motor drive is suitable, particularly for an application to a fan-or blower-like load. The load torque is proportional to a square of the motor mechanical speed, and is changing slow enough to be considered as steady-state conditions. The start-up method discussed in this paper is characterized by combining capacitor voltage control with motor-speed control. The capacitor-voltage control plays a part in regulating the mean dc voltage of each of the dc capacitors and in mitigating the ac voltage appearing across each dc capacitor, which fluctuates at the stator-current frequency. The motor-speed control makes it possible to eliminate a speed sensor from the drive system and to mitigate the ac-voltage fluctuation in all the frequency range. This motor-speed control relies on an equivalent circuit of an induction motor,. It is somewhat similar in basic idea to conventional “volts-per-hertz” or shortly “V/f” and “slip-frequency” control techniques, but different in terms of combining the two control techniques together. The motor-speed control is based on “feedback” control of the stator current, which is the same as that in the slip-frequency control, whereas the commands for the amplitude and frequency of the stator current are based on “feed forward” control in consideration of a speed-versus-load-torque characteristic, as done in the V/f control.

Therefore, neither motor parameter nor speed sensor is required. Furthermore, the motor-speed control is applicable to any inverter equipped with current sensors at the ac terminals.

## II. CIRCUIT CONFIGURATION AND CAPACITOR VOLTAGE CONTROL

Fig. 1 shows the main circuit configuration of the DSCC. The center tap of each inductor is connected directly to each of the stator terminals of an induction motor, where  $i_u$  is the u-phase stator current. The center-tapped inductor is more cost effective than two non coupled inductors per leg, because the center tapped inductor presents inductance  $L_Z$  only to the circulating Current  $i_Z$  and no inductance to the stator current  $i_u$ . It brings significant reductions in size, weight, and cost of the magnetic core. These advantages in the center-tapped inductor are mostly welcomed, particularly applications to motor drives, in which no ac inductors are required between the motor and the inverter.

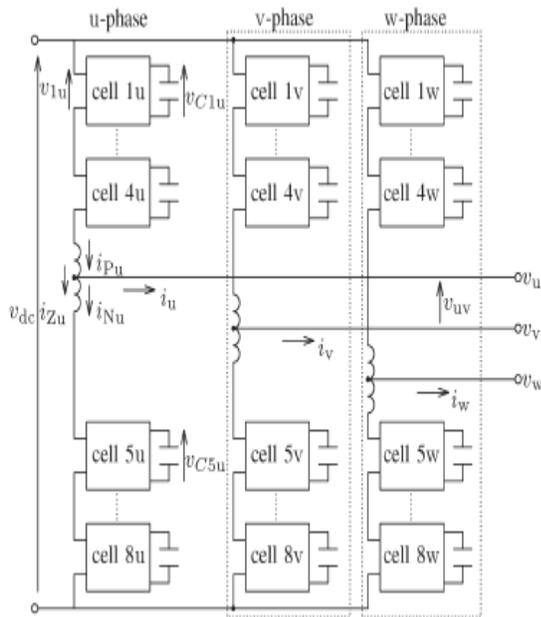


Figure 1. Circuit configuration for an MMCI-DSCC Power Circuit

In Fig. 1, instantaneous currents  $i_{Pu}$  and  $i_{Nu}$  are the u-phase positive- and negative-arm currents, respectively, and  $i_{Zu}$  is the u-phase circulating current defined as follows

$$i_{Zu} = \frac{1}{2} (i_{Pu} + i_{Nu})$$

Note that  $i_{Zu}$  includes dc and ac components to be used for the capacitor-voltage control. The dc component flows from the common dc link to each leg, while the ac component circulates among the three legs. The individual ac

components included in the three-phase circulating currents  $\tilde{i}_{Zu}$ ,  $\tilde{i}_{Zv}$ , and  $\tilde{i}_{Zw}$  cancel each other out, so that no ac component appears in either motor current or dc-link current. The arm currents  $i_{Pu}$  and  $i_{Nu}$  can be expressed as linear functions of two independent variables  $i_u$  and  $i_{Zu}$  as follows

$$i_{Pu} = \frac{i_u}{2} + i_{Zu}$$

$$i_{Nu} = -\frac{i_u}{2} + i_{Zu}$$

## III. MOTOR SPEED CONTROL

This section describes a motor-speed control forming a feedback loop of three-phase stator currents for achieving a stable start-up of an induction motor. First, the motor-speed control is discussed in terms of a form and function. Second, it is compared with conventional motor-speed control techniques, i.e., “volts-per-hertz” and “slip-frequency” control techniques.

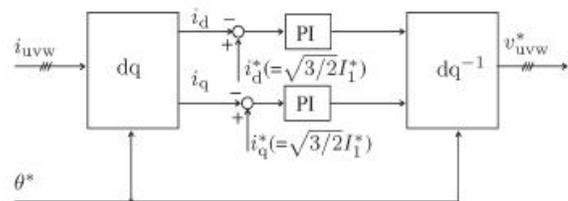


Figure 2. Block diagram for the motor-speed control based on a feedback control of the stator current

The motor-speed control forms a feedback loop of three phase stator currents to realize a stable start-up from standstill. This requires the current sensors attached to the ac terminals. The stator current in one phase is calculated by the corresponding arm currents detected. Therefore, no additional current sensor is required. Fig. 3 shows the block diagram for the motor-speed control. The three-phase stator currents are transformed into dc quantities by using the d-q transformation to enhance current controllability. In Fig. 3,  $\theta^*$  is the phase information used for the d-q transformation, whereas  $i_d^*$  and  $i_q^*$  are the command currents given by

$$i_d^* = i_q^* = \sqrt{\frac{3}{2}} I_1^*$$

where  $I_1^*$  is the command for the stator rms current. Note that  $I_1^*$  and  $f^*$  are given not by feedback control

## IV. COMPARISONS OF THREE MOTOR-SPEED CONTROL TECHNIQUES

The “volts-per-hertz” control or shortly “V/f” control has two independent variables  $V_1$  and  $f$ , in which  $V_1$  is the

stator voltage and  $f$  is the stator frequency. On the other hand, the two dependent variables are the stator current  $I_1$  and the slip frequency  $f_s$ . The V/f control is a straightforward speed control requiring no speed sensor, which is based on feed forward control of  $V_1$  and  $f$ . However, both motor and DSCC may suffer from an overcurrent during the start-up or when a rapid change in torque occurs. The slip-frequency control has two independent variables  $I_1$  and  $f_s$ , and the two dependent variables are  $V_1$  and  $f$ . Here, the commands for  $I_1$  and  $f_s$  are determined by a feedback loop of the motor mechanical speed, thus requiring a speed sensor attached to the motor shaft. The slip-frequency control can provide a faster torque response than the V/f control because of the existence of a feedback control for the motor mechanical speed.

The motor-speed control proposed for the DSCC-based induction motor drive has two independent variables  $I_1$  and  $f$ , and the two dependent variables are  $V_1$  and  $f_s$ . Unlike the slip frequency control, the motor-speed control requires no speed sensor because the commands for  $I_1$  and  $f$ , i.e.,  $I_1^*$  and  $f^*$ , are given not by feedback control, but by feed forward control, as done in the V/f control. This implies that the motor speed control proposed in this paper is inferior to the slip frequency control, in terms of torque controllability. However, it is applicable to a fan- or blower-like load, where the load torque is changing relatively slow and predictable. Moreover, no overcurrent occurs during the start-up, or when a rapid change in torque occurs, because of the existence of a feedback control loop of the stator current. An energy saving during a start-up does not make a significant contribution to total energy saving performance from a practical point of view because the motor power in a low speed range is negligible in applications such as fan- or blower-like loads. This means that a comparison of the three methods, in terms of energy saving performance during a start-up, does not make sense when fan- or blower-like loads are considered. Moreover, current stresses of the conventional motor-speed control techniques, the V/f and slip-frequency control, and the proposed motor-speed control technique are the same, at least, in a steady-state condition when a magnetizing current is set to the same value in all speed range

**V. COMMAND STATOR CURRENT**

The following practical limitations should be imposed on  $I_1^*$

- $I_1^*$  should take the smallest current to produce a desired motor torque  $T_M$ . The maximum value of each arm current is lower than the amplitude of the rated stator current. The first condition should be met because as  $I_1^*$  gets larger,  $\Delta v_{Cju}$  gets higher, as predicted from In other words, minimizing  $I_1^*$  enables minimization of  $\Delta v_{Cju}$ .

The second limitation should be met because an increase in the arm currents brings additional loss to a DSCC and makes the center-tapped inductors larger and heavier. Note that such an increase in the arm currents occurs particularly in low-speed operation, where a large amount of ac circulating current is superimposed on each arm current. The ac circulating current superimposed results in mitigating the ac-voltage fluctuation appearing across the dc capacitor of each chopper cell. Hence,  $I_1^*$  should be minimized because the ac component of the arm current is proportional to  $I_{L1}^*$ .

$$T_M - T_L \geq (J_M - J_L) \frac{d\omega_{rm}}{dt}$$

where  $T_L$  is the load torque,  $J_M$  is the moment of inertia of the motor,  $J_L$  is that of the load, and  $\omega_{rm}$  is the mechanical angular velocity. The right-hand term on (11) corresponds to an acceleration torque for the start up. These three assumptions are applicable to fan- or blower-like loads for the following reasons. The first assumption is valid because the motor frequency, or the motor mechanical speed, is adjusted slowly, e.g., spending a few or several minutes to complete its start-up procedure. The second assumption is reasonable for an induction motor. The third assumption is valid because  $J_L$  is typically 50–100 times larger than  $J_M$

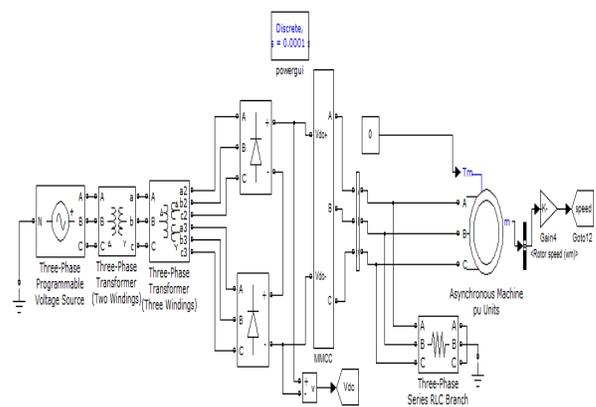


Figure 3. Simulation model

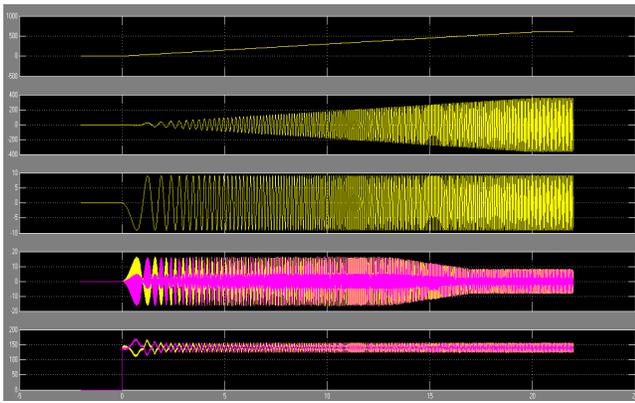


Figure 4. Experimental start-up waveforms when  $I1^* = 6.4$  A (20%) and  $TL = 0\%$ , where  $I0 = 6.4$  A (35%).

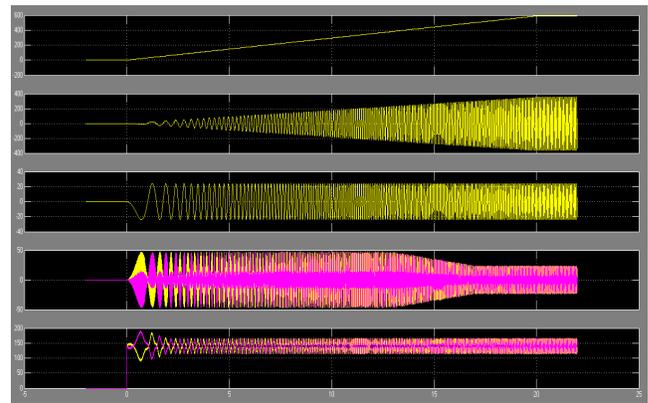


Figure 7. Experimental start-up waveforms when  $I1^* = 14$  A (44%), and  $TL = 40\%$ , where  $I0 = 9.9$  A (54%).

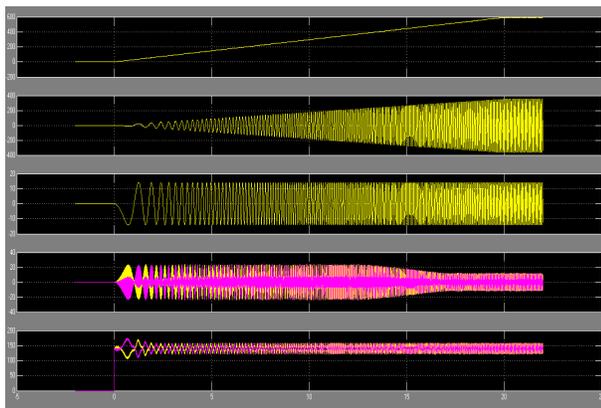


Figure 5. Experimental start-up waveforms when  $I1^* = 10$  A (31%) and  $TL = 20\%$ , where  $I0 = 7.0$  A (38%).

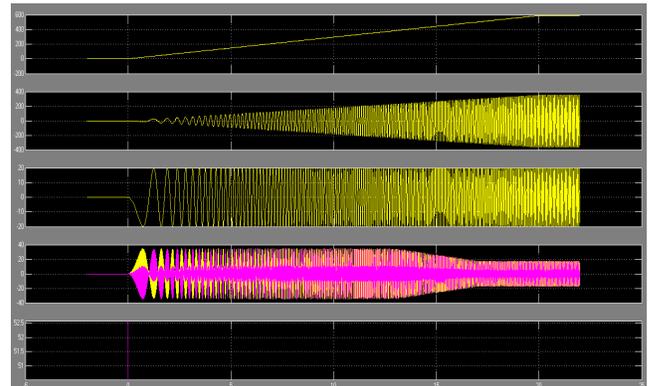


Figure 8. Experimental start-up waveforms when  $I1^* = 17$  A (53%), and  $TL = 60\%$ , where  $I0 = 12.0$  A (65%).

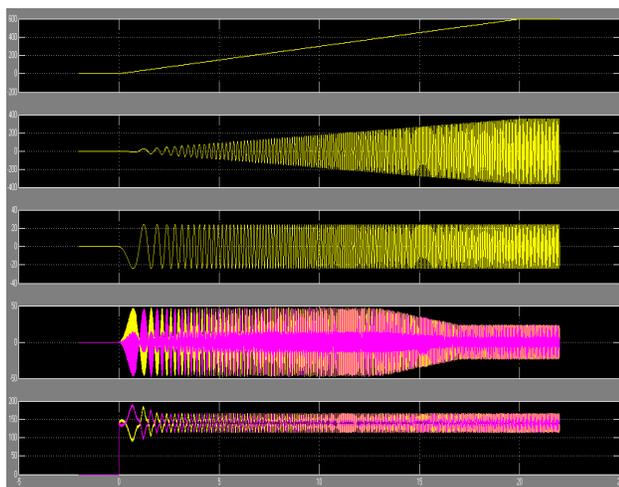


Figure 6. Experimental start-up waveforms when  $I1^* = 17$  A (53%) and  $TL = 20\%$ , where  $I0 = 16.6$  A (90%).

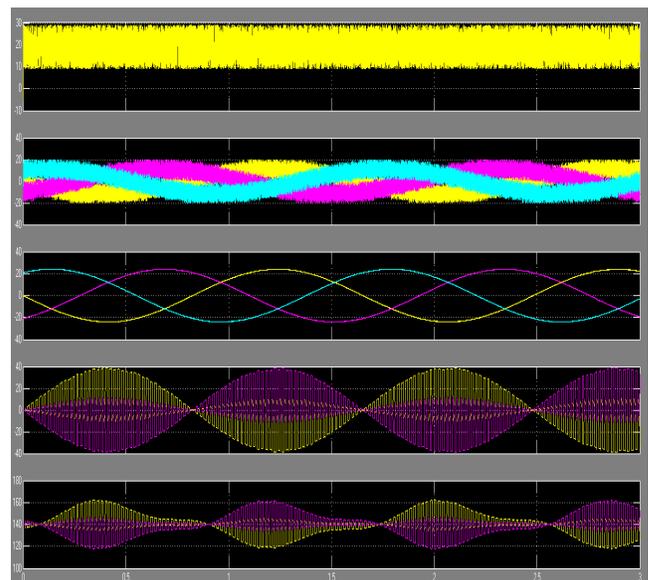


Figure 9. Experimental steady-state waveforms when  $I1^* = 17$  A (53%),  $f^* = 1$  Hz, and  $TL = 60\%$ , where  $I0 = 12.0$  A (65%).

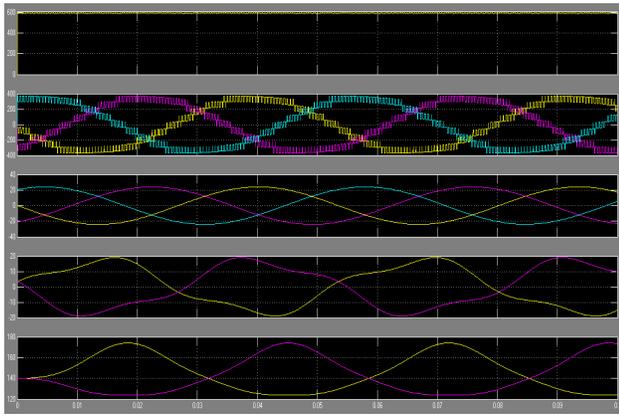


Figure 10. Experimental steady-state waveforms when  $I_1^* = 17$  A (53%),  $f^* = 15$  Hz, and  $TL = 60\%$ , where  $I_0 = 12.0$  A (65%).

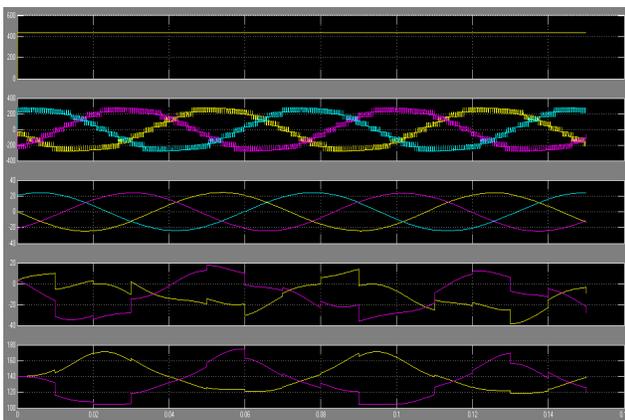


Figure 11. Experimental steady-state waveforms with  $I_1^* = 17$  A (53%),  $f^* = 20$  Hz, and  $TL = 60\%$ , where  $I_0 = 12.0$  A (65%).

## V. CONCLUSION

This paper has proposed a practical start-up method for a DSCC-driven induction motor with no speed sensor from standstill to middle speed. This start-up method is characterized by combining capacitor-voltage control and motor-speed control. The motor-speed control with the minimal stator current under a load torque is based on the combination of feedback control of the three-phase stator currents with feed forward control of their amplitude and frequency. The arm-current amplitudes and ac-voltage fluctuations across each of the dc capacitors can be reduced to acceptable levels. An experimental result obtained from a 400-V 15-kW downscaled system has shown that the motor loaded with 60% can achieve a stable start up from standstill to a middle speed of  $N_{rm} = 588$  min<sup>-1</sup> without overvoltage and over current. The start-up torque has been increasing by a factor of three, without additional stress on both arm currents and ac-voltage fluctuations. This method is suitable

particularly for adjustable-speed motor drives of large-capacity fans, blowers, and compressors for energy savings.

## REFERENCES

- [1] P. W. Hammond, "A new approach to enhance power quality for medium voltage ac drives," *IEEE Trans. Ind. Appl.*, vol. 33, no. 1, pp. 202–208, Jan./Feb. 1997.
- [2] R. Teodorescu, F. Blaabjerg, J. K. Pedersen, E. Cengelci, and P. N. Enjeti, "Multilevel inverter by cascading industrial VSI," *IEEE Trans. Ind. Appl.*, vol. 49, no. 4, pp. 832–838, Jul./Aug. 2002.
- [3] J. Rodriguez, S. Bernet, J. O. Bin Wu, and S. Pontt, "Multilevel voltage-source converter topologies for industrial medium-voltage drives," *IEEE Trans. Ind. Electron.*, vol. 54, no. 6, pp. 2930–2945, Dec. 2007.
- [4] S. Malik and D. Kluge, "ACS 1000 world's first standard ac drive for medium-voltage applications," *ABB Rev.*, no. 2, pp. 4–11, 1998.
- [5] H. Akagi, "Classification, terminology, and application of the modular multilevel cascade converter (MMCC)," *IEEE Trans. Power Electron.*, vol. 26, no. 11, pp. 3119–3130, Nov. 2011.
- [6] A. Lesnicar and R. Marquardt, "An innovative modular multilevel converter topology suitable for a wide power range," in *Conf. Rec. IEEE Bologna PowerTech*, 2003, [CD-ROM].
- [7] M. Hagiwara and H. Akagi, "Control and experiment of pulse-widthmodulated modular multilevel converters," *IEEE Trans. Power Electron.*, vol. 24, no. 7, pp. 1737–1746, Jul. 2009.
- [8] M. Hiller, D. Krug, R. Sommer, and S. Rohner, "A new highly modular medium voltage converter topology for industrial drive applications," in *Conf. Rec. EPE*, 2009, pp. 1–10.
- [9] S. Rohner, J. Weber, and S. Bernet, "Continuous model of modular multilevel converter with experimental verification," in *Conf. Rec. IEEEECCE*, 2011, pp. 4021–4028.
- [10] M. Hagiwara, K. Nishimura, and H. Akagi, "A medium-voltage motor drive with a modular multilevel PWM inverter," *IEEE Trans. Power Electron.*, vol. 25, no. 7, pp. 1786–1799, Jul. 2010.

- [11] A. Antonopoulos, L. Angquist, S. Norrga, K. LIVES, and H. P. Nee, "Modular multilevel converter ac motor drives with constant torque from zero to nominal speed," in Conf. Rec. IEEE-ECCE, 2012, pp. 739–746.
- [12] J. Kolb, F. Kammerer, and M. Braun, "Dimensioning and design of a modular multilevel converter for drive applications," in Conf. Rec. EPE, 2012, pp. LS1a-1.1-1–LS1a-1.1-8, [CD-ROM].
- [13] A. J. Korn, M. Winkelkemper, and P. Steimer, "Low output frequency operation of the modular multilevel converter," in Conf. Rec. IEEE-ECCE, 2010, pp. 3993–3997.
- [14] M. Hagiwara, I. Hasegawa, and H. Akagi, "Startup and low-speed operation of an adjustable-speed motor driven by a modular multilevel cascade inverter (MMCI)," *IEEE Trans. Ind. Appl.*, vol. 49, no. 4, pp. 1556–1565, Jul./Aug. 2013
- [15] J. Holtz, "Sensorless control of induction motor drives," *Proc. IEEE*, vol. 90, no. 8, pp. 1359–1394, Aug. 2002.
- [16] R. J. Pottebaum, "Optimal characteristics of a variable-frequency centrifugal pump motor drive," *IEEE Trans. Ind. Appl.*, vol. 20, no. 1, pp. 23–31, Jan. 1984.
- [17] N. Hirotami, H. Akagi, I. Takahashi, and A. Nabae, "A new equivalent circuit of induction motor based on the total linkage flux of the secondary windings," *Elect. Eng. Japan*, vol. 103, no. 2, pp. 68–73, Mar./Apr. 1983.
- [18] A. Munoz-Garcia, T. A. Lipo, and D. W. Novotny, "A new induction motor V/f control method capable of high-performance regulation at low speeds," *IEEE Trans. Ind. Appl.*, vol. 34, no. 4, pp. 813–821, Jul./Aug. 1998.
- [19] H. Fujita, S. Tominaga, and H. Akagi, "Analysis and design of a dc voltage-controlled static var compensator using quad-series voltage-source inverters," *IEEE Trans. Ind. Appl.*, vol. 32, no. 4, pp. 970–977, Jul./Aug. 1996.
- [20] H. Peng, M. Hagiwara, and H. Akagi, "Modeling and analysis of switching-ripple voltage on the dc link between a diode rectifier and a modular multilevel cascade inverter (MMCI)," *IEEE Trans. Power Electron.*, vol. 28, no. 1, pp. 75–84, Jan. 2013.